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| C  | <table><tr><th>Title</th><th>Sheet</th></tr><tr><td>Cover Page</td><td>1</td></tr><tr><td>UT165-Q46</td><td>2</td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr></table> |  | Title  | Sheet | Cover Page | 1                       | UT165-Q46 | 2 |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | <table><tr><th>Rev.</th><th>Date</th><th>Comments</th></tr><tr><td>V1.1</td><td>2007/12/12</td><td>Sorting the reference number of part</td></tr><tr><td>V1.2</td><td>2008/04/16</td><td>1.Add R20 to pull hi the net of F0_RBn<br/>2.Connector the net of F0_RBn &amp; F1_RBn to get better driving ability</td></tr><tr><td>V1.3</td><td>2008/05/05</td><td>1.Reserve L1 to suppress EMI noise<br/>2.Add some by-pass Caps on Flash to enhance flash power stability</td></tr><tr><td>V1.4</td><td>2008/09/26</td><td>1.Reserve a parallel cap(C8) on DP to pass the new codition of eye-pattern<br/>2.Both support the normal pkg(R17) and Toshiba-43nm pkg(R16)</td></tr><tr><td>V1.5</td><td>2008/10/20</td><td>1.Modify to support the normal pkg and Toshiba-43nm pkg(add R16,R17)</td></tr><tr><td></td><td></td><td></td></tr></table> |  |  | Rev. | Date | Comments | V1.1 | 2007/12/12 | Sorting the reference number of part | V1.2 | 2008/04/16 | 1.Add R20 to pull hi the net of F0_RBn<br>2.Connector the net of F0_RBn & F1_RBn to get better driving ability | V1.3 | 2008/05/05 | 1.Reserve L1 to suppress EMI noise<br>2.Add some by-pass Caps on Flash to enhance flash power stability | V1.4 | 2008/09/26 | 1.Reserve a parallel cap(C8) on DP to pass the new codition of eye-pattern<br>2.Both support the normal pkg(R17) and Toshiba-43nm pkg(R16) | V1.5 | 2008/10/20 | 1.Modify to support the normal pkg and Toshiba-43nm pkg(add R16,R17) |  |  |  | C |
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| V1.4                                       | 2008/09/26   | 1.Reserve a parallel cap(C8) on DP to pass the new codition of eye-pattern<br>2.Both support the normal pkg(R17) and Toshiba-43nm pkg(R16) |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
| V1.5                                       | 2008/10/20   | 1.Modify to support the normal pkg and Toshiba-43nm pkg(add R16,R17)   |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
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| C  | <table><tr><th>Title</th><th>Sheet</th></tr><tr><td>Cover Page</td><td>1</td></tr><tr><td>UT165-Q46</td><td>2</td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr></table> |  | Title  | Sheet | Cover Page | 1                       | UT165-Q46 | 2 |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | <table><tr><th>Rev.</th><th>Date</th><th>Comments</th></tr><tr><td>V1.1</td><td>2007/12/12</td><td>Sorting the reference number of part</td></tr><tr><td>V1.2</td><td>2008/04/16</td><td>1.Add R20 to pull hi the net of F0_RBn<br/>2.Connector the net of F0_RBn &amp; F1_RBn to get better driving ability</td></tr><tr><td>V1.3</td><td>2008/05/05</td><td>1.Reserve L1 to suppress EMI noise<br/>2.Add some by-pass Caps on Flash to enhance flash power stability</td></tr><tr><td>V1.4</td><td>2008/09/26</td><td>1.Reserve a parallel cap(C8) on DP to pass the new codition of eye-pattern<br/>2.Both support the normal pkg(R17) and Toshiba-43nm pkg(R16)</td></tr><tr><td>V1.5</td><td>2008/10/20</td><td>1.Modify to support the normal pkg and Toshiba-43nm pkg(add R16,R17)</td></tr><tr><td></td><td></td><td></td></tr></table> |  |  | Rev. | Date | Comments | V1.1 | 2007/12/12 | Sorting the reference number of part | V1.2 | 2008/04/16 | 1.Add R20 to pull hi the net of F0_RBn<br>2.Connector the net of F0_RBn & F1_RBn to get better driving ability | V1.3 | 2008/05/05 | 1.Reserve L1 to suppress EMI noise<br>2.Add some by-pass Caps on Flash to enhance flash power stability | V1.4 | 2008/09/26 | 1.Reserve a parallel cap(C8) on DP to pass the new codition of eye-pattern<br>2.Both support the normal pkg(R17) and Toshiba-43nm pkg(R16) | V1.5 | 2008/10/20 | 1.Modify to support the normal pkg and Toshiba-43nm pkg(add R16,R17) |  |  |  | C |
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| Rev.                                       | Date   | Comments   |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
| V1.1                                       | 2007/12/12   | Sorting the reference number of part   |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
| V1.2                                       | 2008/04/16   | 1.Add R20 to pull hi the net of F0_RBn<br>2.Connector the net of F0_RBn & F1_RBn to get better driving ability                             |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
| V1.3                                       | 2008/05/05   | 1.Reserve L1 to suppress EMI noise<br>2.Add some by-pass Caps on Flash to enhance flash power stability                                    |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
| V1.4                                       | 2008/09/26   | 1.Reserve a parallel cap(C8) on DP to pass the new codition of eye-pattern<br>2.Both support the normal pkg(R17) and Toshiba-43nm pkg(R16) |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
| V1.5                                       | 2008/10/20   | 1.Modify to support the normal pkg and Toshiba-43nm pkg(add R16,R17)   |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
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| A  |  |  | <table><tr><td colspan="3">Afatech Technology Inc.</td></tr><tr><td colspan="3">TitleCover Page</td></tr><tr><td>SizeA</td><td>Document NumberUT165-Q46</td><td>Rev1.5</td></tr><tr><td colspan="3">Date: Monday, October 20, 2008Sheet 1 of 2</td></tr></table> |       |            | Afatech Technology Inc. |           |   | TitleCover Page |  |  | SizeA | Document NumberUT165-Q46 | Rev1.5 | Date: Monday, October 20, 2008Sheet 1 of 2 |  |  | A |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
| Afatech Technology Inc.                    |  |  |  |       |            |                         |           |   |                 |  |  |       |                          |        |  |  |  |   |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |   |  |  |      |      |          |      |            |                                      |      |            |  |      |            |   |      |            |  |      |            |  |  |  |  |   |
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